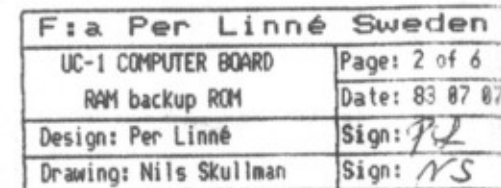
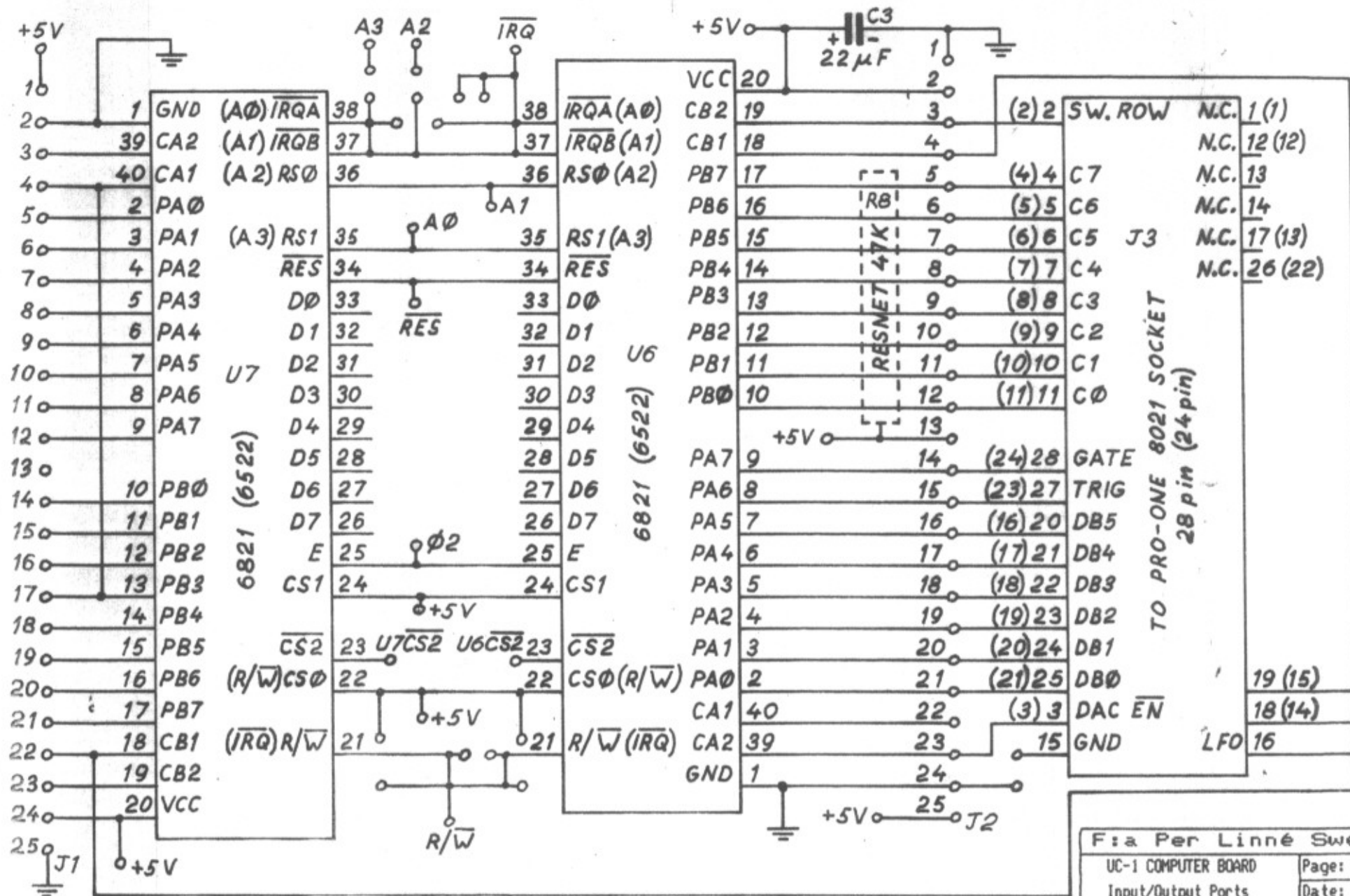
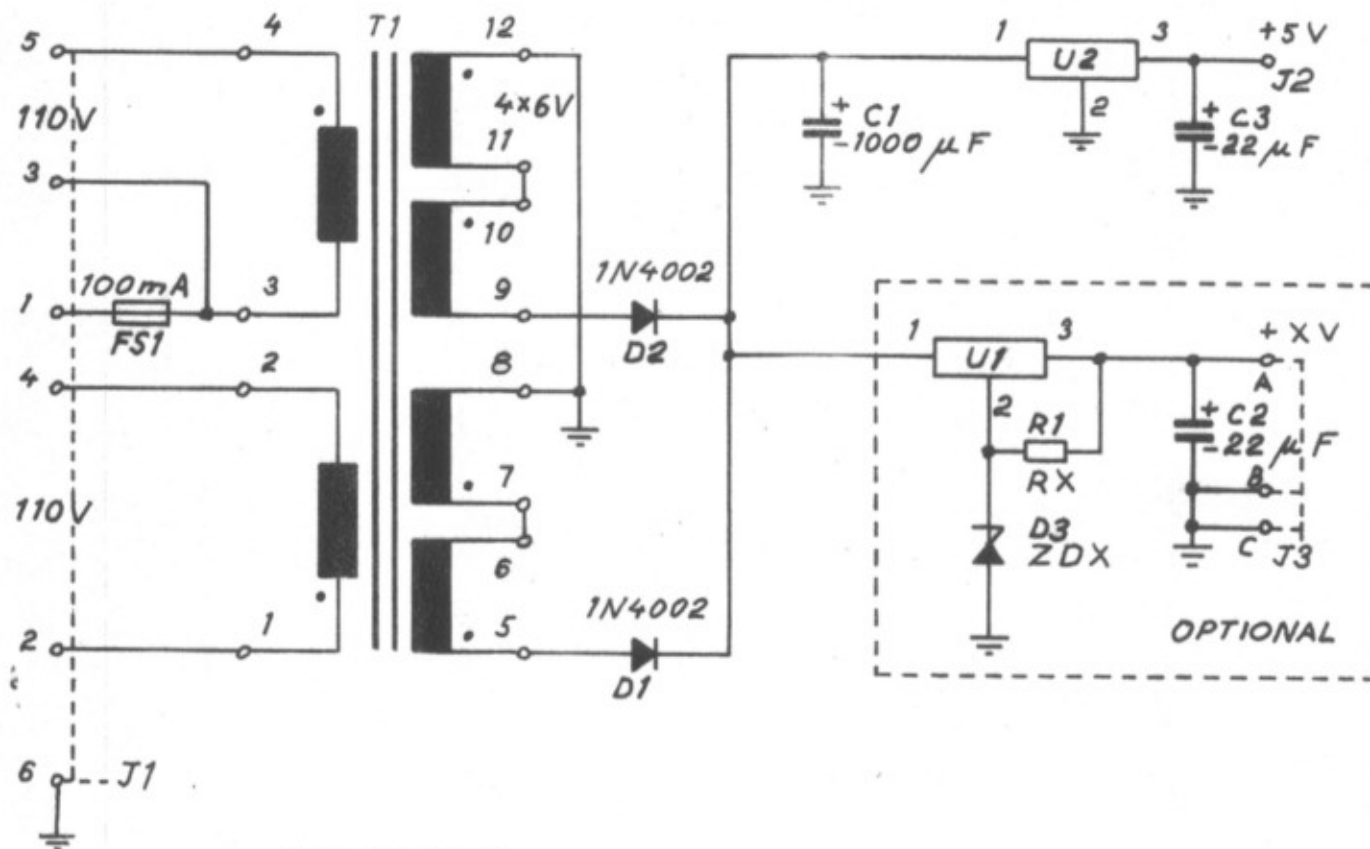


U2= 74LS04
*NOT USED

F:a Per Linné Sweden	
UC-1 COMPUTER BOARD	Page: 1 of 6
CPU clock reset	Date: 83 07 06
Design: Per Linné	Sign: <i>PL</i>
Drawing: Nils Skulman	Sign: <i>NS</i>



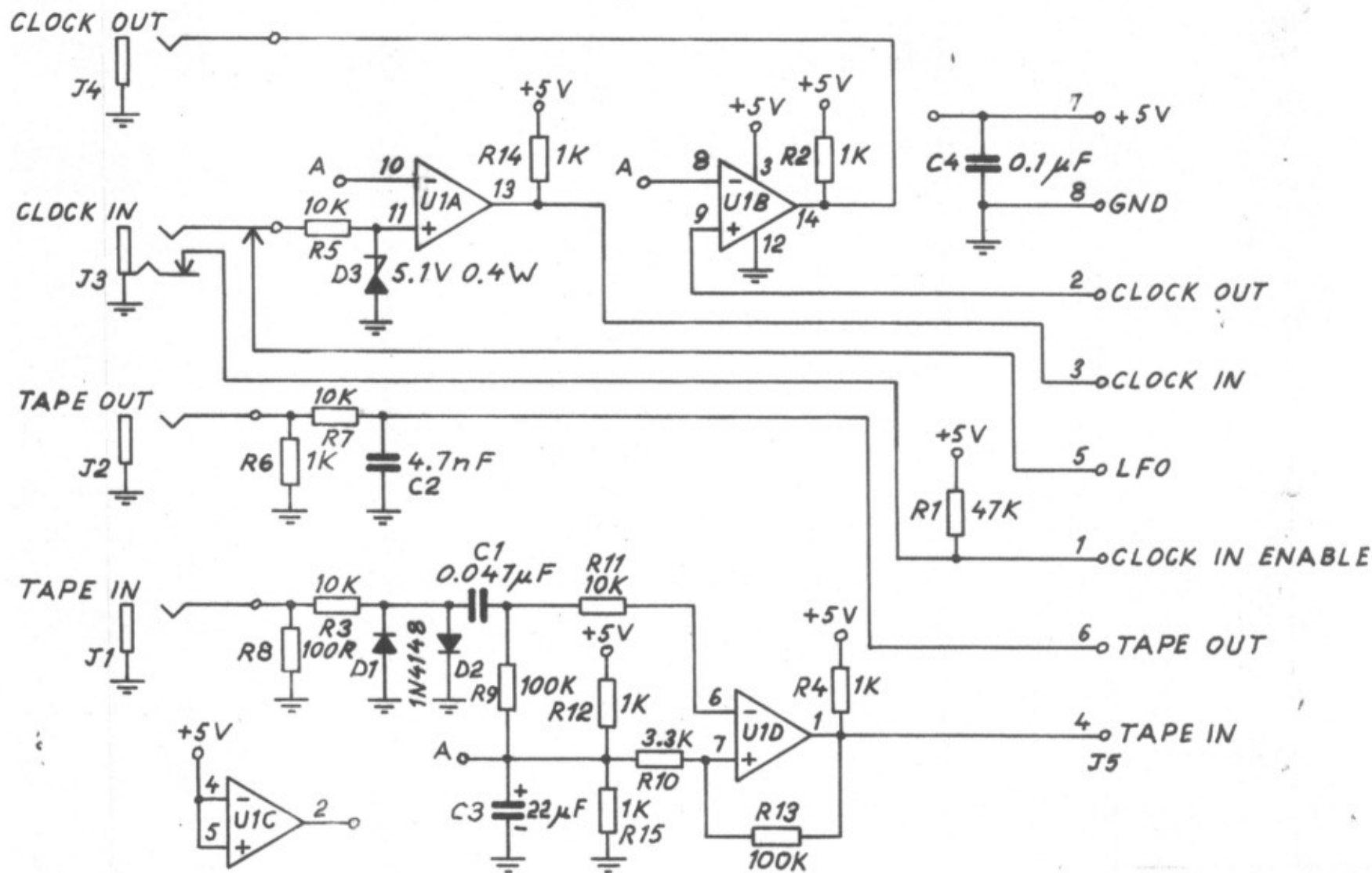




U2 : 78M05

U1 : 78XX

F:a Per Linné Sweden	
UC-1 POWER SUPPLY	Page: 4 of 6
110/220 V	Date: 83 05 27
Design: Per Linné	Sign: <i>PL</i>
Drawing: Nils Skullman	Sign: <i>NS</i>



U1: 3302

F:a Per Linné Sweden	
UC-1 INTERFACE1	Page: 6 of 6
Tape and TTL in/out	Date: 83 06 01
Design: Per Linné	Sign: <i>PL</i>
Drawing: Nils Skulman	Sign: <i>NS</i>

